

INFORMATION DISCLOSURE STATEMENT

Atty. Docket No. 1558

Serial No. 10/036000

Applicant: LEVIN, Harold J.

Filing Date: 12/31/01

Group:

2857

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if appropriate
PP	AA 09/896788		Dionne, Gregory E.	702		29-Jun-2001
	AB					
	AC					
	AD					
	AE					
	AF					

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AG							
	AH							
	AI							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

PP	AJ	Refining the digital spectrum David, P.A.M.-S.; Szczupak, J., Circuits and Systems, 1996., IEEE 39th Midwest symposium on , Volume: 2 , 1996, Page(s): 767 -770 vol.2
PP	AK	Quasi-synchronous sampling algorithm and its applications Xianzhong Dai; Gretsch, R., Instrumentation and Measurement, IEEE Transactions on , Volume: 43 Issue: 2 , April 1994, Page(s): 204 -209
	AL	
	AM	
	AN	
	AO	

Examiner		Date Considered
		11/17/01

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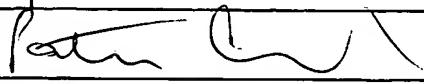
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if appropriate
PM	AA	4686457	Aug 11, 1987	Banno	324	77	Aug 1, 1986
PM	AB	4319329	Mar 9, 1982	Girgis et al.	364	484	Feb 21, 1980
PP	AC	5610847	Mar 11, 1997	Kundert	364	576	Oct 28, 1994
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
PD	AG	EP 0297418	Jun 23, 1988	EPO	G01R 23	15		X
	AH							
	AI							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

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Examiner		Date Considered
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